

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

Patent No.:

Filed:

Issue Date:

Title:

Commissioner for Patents
Washington, D.C. 20231

**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)**

As assignee of record of each of the patents listed in the table of attachment A,

REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given in each of the listed patents are hereby revoked, and

NEW POWER OF ATTORNEY

the following attorneys/agents are hereby appointed to prosecute this patent/application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this patent/application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number:

15650

Patent Trademark Office

Please direct all future correspondence and telephone calls to:

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ASSIGNEE OF ENTIRE INTEREST

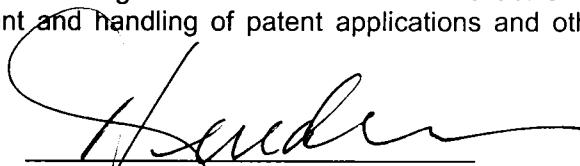
BROADCOM CORPORATION

5300 California Avenue
Irvine, California 92617-3038

ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with Broadcom Corporation, I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date: 9/12/11



Dee Henderson
Director - Intellectual Property Administration
Broadcom Corporation

ATTACHMENT A

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
1	7,206,366	BU2533C1	SYSTEM AND METHOD FOR PROGRAMMABLY ADJUSTING GAIN AND FREQUENCY RESPONSE IN A 10-GIGABIT ETHERNET/FIBRE CHANNEL SYSTEM	06/08/2010	014017/0722
2	6,870,228	BU2534	SYSTEM AND METHOD TO REDUCE NOISE IN A SUBSTRATE	3/22/2005	013786/0440
3	7,781,841	BU2534C1	SYSTEM AND METHOD TO REDUCE NOISE IN A SUBSTRATE	08/24/2010	013786/0440
4	7,361,540	BU2534C5	METHOD OF REDUCED NOISE DISTRIBUTING A SIGNAL IN AN ELECTRONIC DEVICE	04/22/2008	013786/0440
5	6,844,226	BU2534D1	SYSTEM AND METHOD TO REDUCE NOISE IN A SUBSTRATE	1/18/2005	014473/0983
6	6,844,764	BU2539	SYSTEM AND METHOD FOR GENERATING TWO EFFECTIVE FREQUENCIES USING A SINGLE CLOCK	01/18/2005	013856/0277
7	7,283,585	BU2616	MULTIPLE DATA RATE COMMUNICATION SYSTEM	10/16/2007	013764/0172
8	7,606,330	BU2616.1	DUAL-RATE SINGLE BAND COMMUNICATION SYSTEM	10/20/2009	013760/0421

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
9	7,966,422	BU2738	SYSTEM AND METHOD FOR COMMUNICATING BETWEEN SERVERS USING A MULTI-SERVER PLATFORM	6/21/2011	014867/0794
10	7,865,744	BU2437	SYSTEM AND METHOD FOR OPTIMIZING POWER CONSUMPTION IN A MOBILE ENVIRONMENT	1/4/2011	014141/0699
11	7,953,194	BU2523	HANDLING VIDEO TRANSITION ERRORS IN VIDEO ON DEMAND STREAMS	5/31/2011	013340/0916
12	6,995,431	BU2534C2	SYSTEM AND METHOD TO REDUCE NOISE IN A SUBSTRATE	2/7/2006	013786/0440
13	6,906,426	BU2535	TRANSCEIVER HAVING SHADOW MEMORY FACILITATING ON-TRANSCEIVER COLLECTION AND COMMUNICATION OF LOCAL PARAMETERS	6/14/2005	013840/0383
14	7,111,208	BU2538	ON-CHIP STANDALONE SELF-TEST SYSTEM AND METHOD	9/19/2006	013591/0054
15	7,486,121	BU2539C1	SYSTEM AND METHOD FOR GENERATING TWO EFFECTIVE FREQUENCIES USING A SINGLE CLOCK	02/03/2009	013856/0277
16	7,889,783	BU2616C1	MULTIPLE DATA RATE COMMUNICATION SYSTEM	02/15/2011	013764/0172
17	7,239,670	BU2618	PRE-EMPHASIS OF TMDS SIGNALLING IN VIDEO APPLICATIONS	7/3/2007	013920/0784

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
18	6,848,012	BU2616.2	METHOD AND SYSTEM FOR AN ADAPTIVE MULTIMODE MEDIA QUEUE	1/25/2005	013764/0252
19	7,165,130	BU2616.2C1	METHOD AND SYSTEM FOR AN ADAPTIVE MULTIMODE MEDIA QUEUE	01/16/2007	013764/0252
20	6,928,495	BU2616.2D1	METHOD AND SYSTEM FOR AN ADAPTIVE MULTIMODE MEDIA QUEUE	08/09/2005	013764/0252
21	7,342,967	BU2637	SYSTEM AND METHOD FOR ENHANCING PERFORMANCE OF PERSONAL VIDEO RECORDING (PVR) FUNCTIONS ON HITS DIGITAL VIDEO STREAMS	3/11/2008	020276/0243
22	7,149,248	BU2638.1	COMMAND PACKET SYSTEM AND METHOD SUPPORTING IMPROVED TRICK MODE PERFORMANCE IN VIDEO DECODING SYSTEMS	12/12/2006	014058/0441
23	7,577,204	BU2638.111	HITS STREAM REWIND	8/18/2009	014656/0645
24	7,057,664	BU2641	METHOD AND SYSTEM FOR CONVERTING INTERLACED FORMATTED VIDEO TO PROGRESSIVE SCAN VIDEO USING A COLOR EDGE DETECTION SCHEME	6/6/2006	013675/0504

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
25	7,873,110	BU2694	MPEG SMART VIDEO TRANSPORT PROCESSOR WITH DIFFERENT TRANSPORT PACKET HANDLING	1/18/2011	013980/0255
26	6,888,562	BU2735	INTEGRAL EYE- PATH ALIGNMENT ON TELEPHONY AND COMPUTER VIDEO DEVICES USING A PINHOLE IMAGE SENSING DEVICE	5/3/2005	013707/0669
27	7,477,283	BU2735C1	INTEGRAL EYE- PATH ALIGNMENT ON TELEPHONY AND COMPUTER VIDEO DEVICES USING A PINHOLE IMAGE SENSING DEVICE	01/13/2009	013707/0669
28	7,469,012	BU2390.1	SYSTEM AND METHOD FOR TRANSCODING ENTROPY-CODED BITSTREAMS	12/23/2008	013557/0774
29	7,742,466	BU2616.4C1	SWITCHBOARD FOR MULTIPLE DATA RATE COMMUNICATION SYSTEM	06/22/2010	013762/0878
30	7,460,589	BU2632	EYE MONITORING AND RECONSTRUCTION USING CDR AND SUB-SAMPLING ADC	12/2/2008	013836/0819
31	7,324,548	BU2647	TRANSCIVER SYSTEM AND METHOD SUPPORTING VARIABLE RATES AND MULTIPLE PROTOCOLS	1/29/2008	013905/0996

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
32	7,391,788	BU2648	METHOD AND SYSTEM FOR A THREE CONDUCTOR TRANSCEIVER BUS	6/24/2008	013825/0110
33	7,356,076	BU2649	SYSTEM AND METHOD SUPPORTING AUTO-RECOVERY IN A TRANSCEIVER SYSTEM	4/8/2008	013753/0445
34	7,496,133	BU2651	MULTI-RATE ON-CHIP OCN FILTER FOR A TRANSCEIVER SYSTEM	2/24/2009	013565/0980
35	7,391,838	BU2652	TRANSCEIVER SYSTEM AND METHOD SUPPORTING MULTIPLE SELECTABLE VOLTAGE CONTROLLED OSCILLATORS	6/24/2008	013895/0584
36	6,864,752	BU2653	CONFIGURABLE VOLTAGE CONTROLLED OSCILLATOR SYSTEM AND METHOD	3/8/2005	013859/0983
37	6,930,512	BU2658.1	ONE-LEVEL ZERO-CURRENT-STATE EXCLUSIVE OR (XOR) GATE	8/16/2005	013991/0709
38	7,409,056	BU2616.5	SWITCHBOARD FOR DUAL-RATE SINGLE-BAND COMMUNICATION SYSTEM	8/5/2008	013762/0402
39	7,526,312	BU2724	COMMUNICATION SYSTEM AND METHOD IN A HYBRID WIRED/WIRELESS LOCAL AREA NETWORK	4/28/2009	013895/0640

No.	Patent No.	BD No.	Patent Title	Issue Date	Assignment to BD (Reel/Frame)
40	7,519,057	BU2737	SYSTEM AND METHOD FOR COMMUNICATING USING A MULTISERVER PLATFORM	4/14/2009	014225/0121
41	6,859,154	BU2741	METHOD TO OVERLAY A SECONDARY COMMUNICATION CHANNEL ONTO AN ENCODED PRIMARY COMMUNICATION CHANNEL	2/22/2005	014016/0121
42	6,911,922	BU2741C1	METHOD TO OVERLAY A SECONDARY COMMUNICATION CHANNEL ONTO AN ENCODED PRIMARY COMMUNICATION CHANNEL	06/28/2005	014016/0121
43	6,930,621	BU2741C2	METHOD TO OVERLAY A SECONDARY COMMUNICATION CHANNEL ONTO AN ENCODED PRIMARY COMMUNICATION CHANNEL	08/16/2005	014016/0121
44	7,463,651	BU2744	METHOD AND SYSTEM FOR EXPLOITING SPARE LINK BANDWIDTH IN A MULTILANE COMMUNICATION CHANNEL	12/9/2008	014014/0825
45	7,804,855	BU2744C1	METHOD AND SYSTEM FOR EXPLOITING SPARE LINK BANDWIDTH IN A MULTILANE COMMUNICATION CHANNEL	09/28/2010	014014/0825

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46	7,835,363	BU2745	METHOD AND SYSTEM TO PROVIDE BLADE SERVER LOAD BALANCING USING SPARE LINK BANDWIDTH	11/16/2010	014544/0575
47	7,236,207	BU2176.1	SYSTEM AND METHOD OF TRANSMISSION AND RECEPTION OF PROGRESSIVE CONTENT WITH ISOLATED FIELDS FOR CONVERSION TO INTERLACED DISPLAY	6/26/2007	014517/0895
48	7,876,380	BU2176.1C1	SYSTEM AND METHOD OF TRANSMISSION AND RECEPTION OF PROGRESSIVE CONTENT WITH ISOLATED FIELDS FOR CONVERSION TO INTERLACED DISPLAY	01/25/2011	014517/0895
49	7,053,720	BU2653C1	CONFIGURABLE VOLTAGE CONTROLLED OSCILLATOR SYSTEM AND METHOD INCLUDING DIVIDING FORMING A PORTION OF TWO OR MORE DIVIDER PATHS	05/30/2006	016859/0983
50	7,386,084	BU2658.1	METHOD AND SYSTEM FOR PATTERN-INDEPENDENT PHASE ADJUSTMENT IN A CLOCK AND DATA RECOVERY (CDR) CIRCUIT	6/10/2008	015594/0368
51	6,989,715	BU2658.1C1	ONE-LEVEL ZERO-CURRENT-STATE EXCLUSIVE OR (XOR) GATE	01/24/2006	013991/0709